

**IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS: Ramesh Yarlagadda, Shwetal Desai, and Harish Devanagondi  
SERIAL NO.: 10/734,082  
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TITLE: High Bandwidth Memory Management Using Multi-Bank DRAM  
Devices  
EXAMINER: Woo H. Choi  
GROUP ART UNIT: 2189  
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MAIL STOP AMENDMENT  
COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

**AMENDMENT C AND RESPONSE UNDER 37 CFR § 1.111**

Sir:

The following amendments and remarks are in response to the Non-Final Office

Action dated January 16, 2007.